

**IN THE DRAWINGS**

Please amend FIG. 1 to include textual descriptions for Controller (16), Memory Control Logic (14), SRAM (20), Memory Array (12), Low Voltage Power Supply (24) and High Voltage Power Supply (26).

Please amend FIG. 2 to include textual descriptions for Memory Array (12), Buffers (122), Over-Protection Detection Circuit (106), Programming Module (102), Latch (108), Address Generator (110), Data Pipeline (112) and Data Buffer (100).

Please amend FIG. 3 to include numerical reference characters 202, 212 and 216 as described in the specification.

Please amend FIG. 4 to include textual descriptions for Over-Program Detection Circuit (106), Buffer (316), Data Buffer (100) and Address Generator (110).

Replacement Drawings are included with this response.